



Docket No.: 0152-0703PUS1  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Hisashi YAMADA et al.

Application No.: 10/524,013

Confirmation No.: 7576

Filed: February 8, 2005

Art Unit: 2823

For: METHOD FOR PRODUCING COMPOUND  
SEMICONDUCTOR WAFER AND  
COMPOUND SEMICONDUCTOR DEVICE

Examiner: M. Estrada

**AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INTRODUCTORY COMMENTS**

In response to the Office Action dated April 17, 2006, please amend the above-identified U.S. patent application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Amendments to the Drawings** begin on page 4 of this paper and include an attached replacement sheet.

**Remarks/Arguments** begin on page 5 of this paper.